


Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)													
Choice Based Credit System (CBCS) Scheme													
Bachelor of Technology (Electronics & Communication)													
III - SEMESTER(w.e.f. Batch 2018-19)													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses- ment*	End Sem University Exam	Teachers Asses- ment*		
1	ML301	Environment and Energy Studies		3	0	0	60	20	20	0	0	100	3
2	BTEC302	Network Analysis and Synthesis		3	1	2	60	20	20	30	20	150	5
3	BTEC303	Electronic Measurement and Instrumentation		3	1	2	60	20	20	30	20	150	5
4	BTEC305	Electronic Circuits		3	1	2	60	20	20	30	20	150	5
5	BTIT309	Introduction to Core JAVA		2	0	2	60	20	20	30	20	150	3
6	BTEC306	Personality Development		0	0	2	0	0	0	0	50	50	1
7	BTEC307	PCB Designing Lab		0	0	4	0	0	0	30	20	50	2
TOTAL				14	3	14	300	100	100	150	150	800	24
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.													
* Students are advised to take online MOOCS course of minimum two credits													


Chairperson
Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


Registrar
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 INDORE (M.P.)